Summary of AD8158 Datasheet Changes for PCN

1. Table 1: Clarified units to be “mV p-p diff” consistently for the following specs:
   a. Differential Input Voltage Swing
   b. Output Voltage Swing
   c. LOS DC Assert Level
   d. LOS DC Deassert Level
2. Table 1: Changed typical specification for LOS Assert and Dessert Levels to be consistent with units “mV p-p diff”. This is not a change in the specification of the device, but rather the factor of 2 change is due to the change in units. The change in units adds consistency between all swing related specs in Table 1.
   a. LOS Assert Level changed from 25mV diff to 50mV p-p diff
   b. LOS Deassert Level changed from 150 mV diff to 300mV p-p diff
3. Table 1: Re-ordered Supply Current specification to improve clarity.
4. Table 1: Changed supply current specifications condition to be ac-coupled inputs and outputs instead of dc-coupled input and outputs. This is not a change to the behavior of the device, but is consistent with the most common applications.

Supply Current	Rev 0. Spec		Rev A. Spec	
	Typical	Max	Typical	Max
I_{TTI}	94mA	107mA	10mA	20mA
I_{CC}	No Change			
I_{TTO}	No Change			
I_{DVCC}	No Change			

5. Table 1: Added output logic level specifications

Parameter	Conditions	Min	Typ	Max	Unit
Output High (V_{OH})	2 kΩ pull-up resistor to DV_{CC}		DV_{CC}		V
Output Low (V_{OL})	I_{OL} = +3 mA	V_{EE}		0.4	V

6. Table 2: Added specification for minimum Reset Pulse Width (10ns minimum)
7. Table 3: Deleted Lead Temperature spec. This spec does not apply to a leadless package.
8. Table 3: Added Junction Temperature spec (125°C)
9. Table 4: Added “Active High” clarification to description column where applicable.
10. Page 13: Replaced Figure 24.
11. Table 8: Fixed typo. Mixed mode control is Mode[1:0] = 10b, not 01b.
12. Changed default register values for RX [A/B/C] LOS Control and RX[A/B/C] LOS Threshold Registers to match the recommended settings. This change affects the Theory of Operation and Register Map sections.

Register Name	Old Default (Rev 0.)	New Default (Rev A.)
RX [A/B/C] Control	0x07	0x05
RX [A/B/C] Threshold	0x1C	0x10

13. Pages 15-28: Cosmetic changes to Theory of Operations section.
    a. Reformatted Table 5 and consolidated information from Tables 7 and 8 into Table 5. Deleted Tables 7 and 8.
    b. Added Equalizer Settings Table.
    c. Reformatted Tables 12-16.
    d. Moved Output Compliance sub-section to the Applications Information section.
    e. Deleted Figure 42.
14. Pages 29-30: Correct errors in Figures 43 and 44. Device address in incorrect in the figures.
15. Added signal conditioning application diagram to Applications Information section.
16. Added multiple supply operation figure to Applications Information section.
17. Added pcb assembly guidelines to Applications Information section.